



Date: May 12, 2004 Page 1 of 1

FORM PTO-1449 (Colb)	ATTY DOCKET NO. 206,209	SERIAL NUMBER 10/634,406
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS' INFORMATION STATEMENT	APPLICANT Amir LEHR, et al.	EXAMINER (N/A)
	FILING DATE August 4, 2003	GROUP ART UNIT (N/A)

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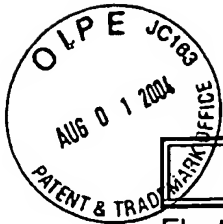
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NT/	AA	6,480,510	11-2002	Binder			
NT/	AB	6,301,527	10-2001	Butland, et al.			
NT/	AC	2003/0058085	03-2003	Fisher, et al.			
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NT/	AG	6,125,448	09-2000	Schwan, et al.			
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NT/	AI	6,377,874	04-2002	Ykema			

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	AJ						
	AK						

OTHER ART (Including Author, Bills, Pertinent Pages, Etc.)

AL	
AM	
EXAMINER: /Vincent Tran/	DATE CONSIDERED: 03/13/2007
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	



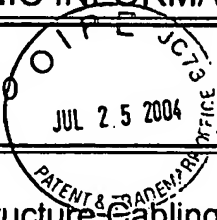
ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of Invention	Structure Cabling System						
Application Number: 10/634406							
Confirmation Number: 7790							
First Named Applicant: Amir Lehr							
Attorney Docket Number: PDS-003C2							
Search string: (5148144 or 5652893 or 6140911 or 6329906 or 6496105 or 6753761 or 20030058085).pn.							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
VT/	1	5148144	1992-09-15	Sutterlin et al			
VT/	2	5652893	1997-07-29	Ben Meir et al			
VT/	3	6140911	2000-10-31	Fisher et al			
VT/	4	6329906	2001-12-11	Fisher et al			
VT/	5	6496105	2002-12-17	Fisher et al			
VT/	6	6753761	2004-06-22	Fisher et al			
US Published Applications							
Note: Applicant is not required to submit a paper copy of cited US Published Applications							
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VT/	1	20030058085	2003-03-27	Fisher et al			
Signature							
Examiner Name				Date			
/Vincent Tran/				03/13/2007			

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Title of
Invention

Structure Cabling System

Application Number: 10/634406
Confirmation Number: 7790
First Named Applicant: Amir LEHR
Attorney Docket Number: PDS-003C2
Art Unit: 2661
Search string: (4939770).pn.



US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
NT	1	4939770	1990-07-03	Makino		379	61


Signature

Examiner Name	Date
/Vincent Tran/	03/13/2007

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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Title of Invention	Structure Cabling System																
 Application Number : 10/634406 Confirmation Number: 7790 First Named Applicant: Amir LEHR Attorney Docket Number: PDS-003C2 Art Unit: 2661 Examiner: Search string: (5991885).pn																	
																	
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<table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>/VT/</td><td>1</td><td>5991885</td><td>1999-11-23</td><td>Change et al</td><td></td><td>713</td><td>300</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	/VT/	1	5991885	1999-11-23	Change et al		713	300
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/Vincent Tran/	03/13/2007																

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Stylesheet Version v18.0

Title of Invention	Stucture Cabling System																										
<p>Application Number : 10/634406 </p> <p>Confirmation Number: 7790</p> <p>First Named Applicant: Amir LEHR</p> <p>Attorney Docket Number: PDS-003C2</p> <p>Art Unit: 2661</p> <p>Examiner:</p> <p>Search string: (6348874).pn</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>/VT/</td><td>1</td><td>6348874</td><td>2002-02-19</td><td>Cole et al</td><td>B1</td><td>340</td><td>825.01</td></tr></tbody></table> <p>Signature</p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td>/Vincent Tran/</td><td>03/13/2007</td></tr></tbody></table>								init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	/VT/	1	6348874	2002-02-19	Cole et al	B1	340	825.01	Examiner Name	Date	/Vincent Tran/	03/13/2007
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10/218,739

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ATTY DOCKET NO.
204,202SERIAL NUMBER
09/365,584LIST OF PATENTS AND PUBLICATIONS FOR
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STATEMENTAPPLICANT
Amir LEHR et al.FILING DATE
August 2, 1999GROUP ART UNIT
2745

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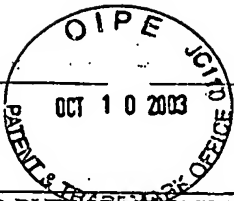
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	AA	5,799,196	Aug. 1998	Flannery			
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AN	Bearfield, J.M., "Control the Power Interface of USB's Voltage Bus" Electronic Design, U.S., Penton Publishing, Cleveland, Ohio, vol. 45, no. 15, July 1997, pg. 80, 82, 84, 86.
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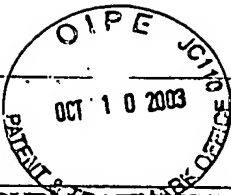
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Application/Control No.

09/365,584

Applicant(s)/Patent Under
Reexamination
LEHR ET AL.

Examiner

Nick Corsaro

Art Unit

2684

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	B	US-5,884,086	03-1999	Amoni et al.	700/282
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
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	J	US-			
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	N	WO 9623377 A1	08-1996	World Intellectual	HUNTER et al.	H04L 12/10
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	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

U.S. Patent and Trademark Office
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